

Mahatma Education Society's

Pillai College of Engineering, New Panvel

DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING

TIME TABLE (IA-2)

CLASS: T.E. EXTC

SEM- V

Day	Date	Time	Subject
Thursday	21st Oct 2021	10:30am - 11:30am	DCOM
Thursday	21st Oct 2021	2:30pm - 3:30pm	DTSP
Friday	22nd Oct 2021	10:30am - 11:30am	DVLSI
Friday	22nd Oct 2021	2:30pm - 3:30pm	RSA
Saturday	23rd Oct 2021	10:30am - 11:30am	<i>Elective :</i> <u>DCC</u> <u>DSA</u>

Prof. Ameet Mehta

(Test Coordinator)

Prof. Florence Simon

(Test Coordinator)

Dr. Avinash Vaidya

(HOD, EXTC)