

Mahatma Education Society's  
Pillai College of Engineering, New Panvel  
Department of Electronics and Telecommunication Engineering

**Internal Assessment 1 Time Table**

**DSE SE EXTC, Semester III, A.Y. 2021-22**

<b>Day</b>	<b>Date</b>	<b>Time</b>	<b>Subject</b>
Friday	7th January 2022	11am to 12pm	Electronic Devices & Circuits
Friday	7th January 2022	3pm to 4pm	Network Theory
Saturday	8th January 2022	11am to 12pm	Electronic Instrumentation & Control Systems
Saturday	8th January 2022	3pm to 4pm	Digital System Design
Sunday	9th January 2022	3pm to 4pm	Engineering Mathematics-III

**Dr. Avinash Vaidya**

**(HOD, EXTC)**